

REMARKS

At the outset, the Examiner is thanked for the thorough review and consideration of the pending application. The final Office Action dated February 05, 2008 and the Advisory Action dated June 19, 2008 have been received and their contents carefully reviewed.

Claims 5, 18, 20, 22 and 27 are hereby amended, claims 6, 19, 21, 23-26 and 28-31 are hereby cancelled and claim 32 is hereby added. Reexamination and reconsideration of the pending claims are respectfully requested.

In the Advisory Action, the Examiner stated that regarding claims 5, 18 and 27, Nanno clearly teaches a power supply circuit in the save field of endeavor being supplied qith a 1.8V power source voltage.

In addition, the Examiner stated that regarding claim 22, VDD2 of Tsutsui is variable in one instance it can be 5V but in the power saving instance it is 3V.

Regarding claims 5, 18 and 27, power supply circuit 24 of Nanno corresponds to DC-DC converter of the claimed invention. Specially, a 1.8V power source voltage stated by the Examiner corresponds to a gate low voltage of the DC-DC converter of the claimed invention.

Thus, a 1.8V power source voltage of Nanno is supplied to the power supply circuit 24, but is not supplied to a gate driver circuit 21, a source diver circuit 22 and a compensation voltage-applying driving circuit 23.

In contrast, a power source voltage less than 3.0V of the claimed invention is directly supplied to an interface circuit, a timing controller, a gate driving circuit and a data driving circuit.

Therefore, Nanno fails to teach this feature of the claimed invention.

Regarding claim 22, a power supply circuit 350 of Tsutsui corresponds to a DC-DC converter of the claimed invention, and VDD2 stated by the Examiner is generated from the power supply circuit 350 and is supplied to an amplifier 14 of a driving circuit 101.

In contrast, a second power source voltage less than 3.0V of the claimed invention is not generated from a power supply circuit but is generated from a reducing circuit, and is supplied to an interface circuit, a timing controller, a data driving circuit and a gate driving circuit.

Therefore, VDD2 of Tsutsui fails to teach this feature of the claimed invention.

None of the cited reference, singly or in combination, teaches or suggests at least this feature of the claimed invention. Accordingly, Applicant respectfully submits that claims 5, 18, 20, 22, 27 and 32 are allowable over the cited references.

Conclusion

In view of the foregoing, Applicant respectfully requests reconsideration and the timely allowance of the pending claims. Should the Examiner feel that there are any issues outstanding after consideration of the Amendment, the Examiner is invited to contact the Applicants' undersigned representative to expedite prosecution.

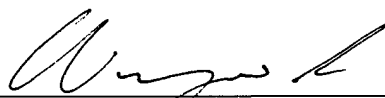
EXCEPT for issue fees payable under 37 C.F.R. 1.18, the Commissioner is hereby authorized by this paper to charge any additional fees during the entire pendency of this application including fees due 37 C.F.R. 1.16 and 1.17 which may be required, including any required extension of time fees, or credit any overpayment to Deposit Account No 50-0310.

This paragraph is intended to be **CONSTRUCTIVE PETITION FOR EXTENSION OF TIME** in accordance with 37 C.F.R. 1.136(a)(3).

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